

The AMBA 3 AHB Lite (Advanced High-performance Bus) is a highly flexible and configurable verification IP that can be easily integrated into any SOC verification environment. Supporting both UVM and OVM, this AHB Lite VIP is part of the asureVIP portfolio of implementation-proven VIP offerings.

The Master and Slave components have been interoperability tested and come with a Bus Monitor for performing all protocol checks. The monitor also performs key protocol checks and reports errors for non-compliance with the AHB specifications.

Customers using the asureVIP products do so with the confidence of knowing that they have been independently developed by Tessolve and successfully deployed by leading SoC companies around the world.

Tessolve can also offer asureVIP customers an independent hardware verification service (asureVERIF) that not only reduces development costs and time-to-market, but also improves product quality.

Overview

- VIP: AMBA ACE (AXI Coherency Extensions)
- Compliance: AMBA AXI Specification
- Language: System Verilog
- Methodology: OVM 2.1.1 / UVM 1.1
- Simulators: Cadence Incisive, Mentor Questa, Aldec Riviera-PRO

AHB Lite VIP Deliverables

- AHB Lite Master or Slave VIP
- Sample Virtual Sequencer
- Sample Scoreboard
- VIP User Guide

Find out more

For more information or to discuss your requirements, please Contact Us www.tessolve.com